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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/039,034	01/02/2002	Tony S. Rand	42390P12361	2349
8791	7590 10/28/2004		EXAM	INER
	SOKOLOFF TAYLOR HIRE BOULEVARD	LANE, J	LANE, JOHN A	
SEVENTH FLOOR		ART UNIT	PAPER NUMBER	
LOS ANGEI	LES, CA 90025-1030		2188	<u> </u>

DATE MAILED: 10/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/039,034	RAND, TONY S.			
Office Action Summary	Examiner	Art Unit			
	Jack A Lane	2188			
The MAILING DATE of this communication Period for Reply	appears on the cover sheet with	h the correspondence address			
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFf after SIX (6) MONTHS from the mailing date of this communication - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st - Any reply received by the Office later than three months after the m earned patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may a rej reply within the statutory minimum of thirty riod will apply and will expire SIX (6) MONT atute, cause the application to become ABA	ply be timely filed (30) days will be considered timely. HS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 2	1 September 2004.				
2a)⊠ This action is FINAL . 2b)□ 1	This action is non-final.				
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims					
4) ⊠ Claim(s) 1-7 and 9-30 is/are pending in the 4a) Of the above claim(s) is/are withe 5) ☐ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-7 and 9-30 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and	drawn from consideration.				
Application Papers					
9) ☐ The specification is objected to by the Exam	niner.				
10)☐ The drawing(s) filed on is/are: a)☐ a	The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.				
Applicant may not request that any objection to	the drawing(s) be held in abeyand	e. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the cor 11) The oath or declaration is objected to by the					
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for force a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the priority docum application from the International But * See the attached detailed Office action for a	ents have been received. ents have been received in Appriority documents have been reau (PCT Rule 17.2(a)).	oplication No received in this National Stage			
Attachment(s)	, —	(DTO 440)			
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 	· —	ummary (PTO-413) /Mail Date			
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB Paper No(s)/Mail Date		formal Patent Application (PTO-152)			

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DETAILED ACTION

- 1. This Office action is responsive to the amendment filed 09/21/2004. Claim 8 has been canceled. Claims 1-7 and 9-30 are presented for examination. Any objections or rejections made in the previous office action not specifically repeated below are withdrawn. The IDS statement submitted 09/21/2004 has not been considered for reasons below.
- 2. The examiner appreciates the substantial response to the request for 102 or 103 prior art. However, the request also stated "[I]n the event prior art documentation is submitted a discussion of relevant passages, figs. etc. is requested." The examiner is looking for specific references to 102/103 prior art that identify claim limitations. Since applicant is most knowledgeable of the submitted prior art and the present invention, his/her discussion of the reference(s) with respect to the claims is essential. In exchange, for this information, the fee and certification requirements of 37 CFR section 1.97 have waived. It is the examiner's intention, through this request, to distinguish the truly patentable features from the more commonly known features.
- 3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

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(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

4. Claims 1-7 and 9-30 are rejected under 35 U.S.C. § 102(e) as being anticipated by Mackenthun et al. (Pat. No. 6,374,332).

Mackenthun teaches the claimed "input-output interface" as circuitry including interface 370 shown in figure 4. The claimed "first address" corresponds to a read request on line 410. The claimed "address queue" corresponds to circuitry including write buffer logic 434, write request 2 logic 438 and write request 1 logic 454 for storing all pending write addresses. The claimed "second address of a pending transaction" corresponds to all pending write addresses. The claimed "address logic circuitry" corresponds to circuitry for comparing read request addresses to pending write addresses as discussed at col. 15, lines 10-15. The claimed "ownership pre-fetch circuitry" corresponds to circuitry including snoop logic circuitry 432. The claimed "transaction" queue" corresponds to circuitry including read request register 412, write request buffer logic 434, write request 2 logic 438 and write request 1 logic 454. The claimed "memory device coupled with said transaction queue" corresponds to circuitry including storage device 1, storage device 2, tag ram 414 and/or data storage devices 419. The claimed "cache" corresponds to first level cache (FLC) 404 and/or second level cache (SLC) 360. The claimed "transaction bypass circuitry" could correspond to circuitry for accessing the Application/Control Number: 10/039,034

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third level cache (TLC) 310 and/or crossbar module (TCM) 220 for accessing main storage 110.

The examiner believes all of the dependent claim features are taught by Mackenthun. That is, the more well known claim features commonly found in the dependent claims are explicitly or inherently found in Mackenthun. For example, claim 4 discusses invalidation. Invalidating data is discussed throughout Mackenthun (see the background of the invention and detailed description) and is generally a well known feature of cache coherency. Many of the claim features, while part of the invention, appear to be well known and their relevance not essential to the main invention found in the independent claim(s). Thus, a detailed discussion of the well known claim feature(s) is not warranted at this time.

In the Remarks filed 09/21/2004 applicant argues:

Mackenthun, however, appears to be silent as to fetching ownership for transactions of **input-output devices** as required by the invention of claims 1-7 and 9-30.

In response, the examiner contends instruction processor 350A (figure 4) corresponds to the claimed "input-output device." Instruction processor accepts data and outputs data. Alternately, it appears the third level cache (TLC) can be considered an input-output device.

Applicant further argues:

Each of claims 7, 20, 22 and 29 are further allowable since they require fetching ownership based upon hub identifications associated with

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transactions. Again, Mackenthun appears to be silent in regard to hub identifications and basing ownership fetching upon such hub identifications.

In response, the claimed "hub identification" corresponds to information included in the request identifying one request from another.

- 5. Applicant's arguments filed 09/21/2004 have been fully considered but they are not deemed to be persuasive.
- 6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 C.F.R. § 1.136(a).
- 7. A SHORTENED STATUTORY PERIOD FOR RESPONSE TO THIS FINAL ACTION IS SET TO EXPIRE THREE MONTHS FROM THE DATE OF THIS ACTION. IN THE EVENT A FIRST RESPONSE IS FILED WITHIN TWO MONTHS OF THE MAILING DATE OF THIS FINAL ACTION AND THE ADVISORY ACTION IS NOT MAILED UNTIL AFTER THE END OF THE THREE-MONTH SHORTENED STATUTORY PERIOD, THEN THE SHORTENED STATUTORY PERIOD WILL EXPIRE ON THE DATE THE ADVISORY ACTION IS MAILED, AND ANY EXTENSION FEE PURSUANT TO 37 C.F.R. § 1.136(a) WILL BE CALCULATED FROM THE MAILING DATE OF THE ADVISORY ACTION. IN NO EVENT WILL THE STATUTORY PERIOD FOR RESPONSE EXPIRE LATER THAN SIX MONTHS FROM THE DATE OF THIS FINAL ACTION.

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Any response to this final action should be mailed to: Box AF

Under Secretary of Commerce for Intellectual Property and Director of the United States Patent and Trademark Office

PO Box 1450

Alexandria, VA 22313-1450

or faxed to:

(703) 872-9306, (for Official communications intended for entry)

Or:

(703) 872-9306, (for Non-Official or Draft communications, please label "Non-Official" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington. VA., Sixth Floor (Receptionist).

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jack A. Lane whose telephone number is 571 272-4208. The examiner can normally be reached on Mon-Fri from 7:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 571 272-4210.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 571 272-2100

JACK A. LANE PRIMARY EXAMINER